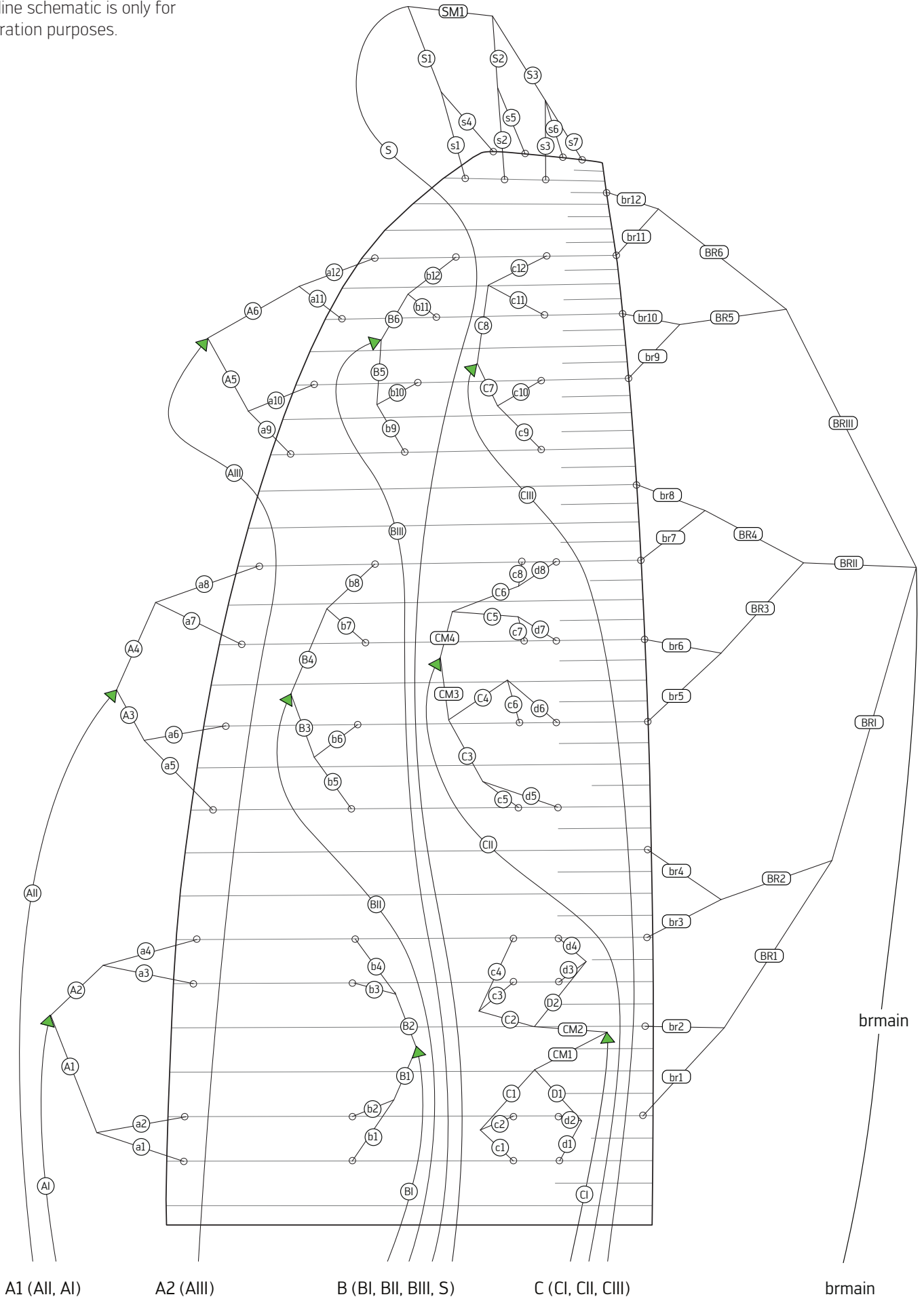


LINE SCHEMATIC

This line schematic is only for illustration purposes.



A1 (AI, AII)

A2 (AIII)

B (BI, BII, BIII, S)

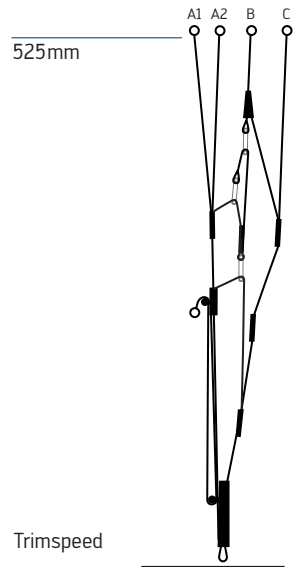
C (CI, CII, CIII)

brmain

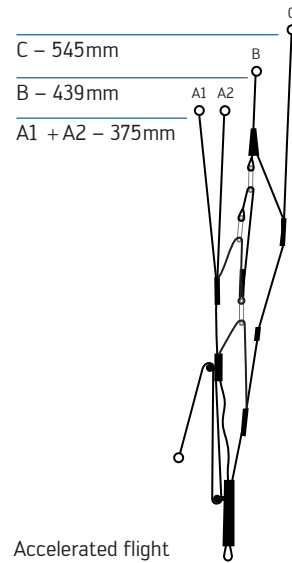
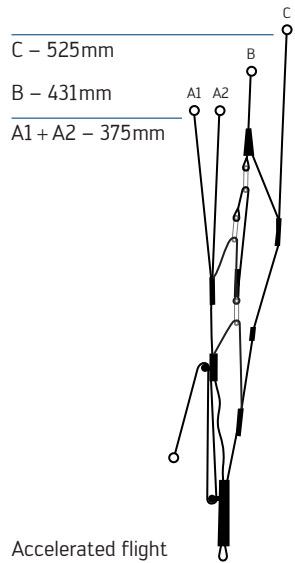
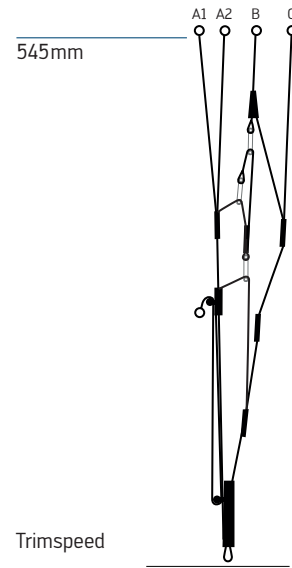
▲ Loopcover

RISERS

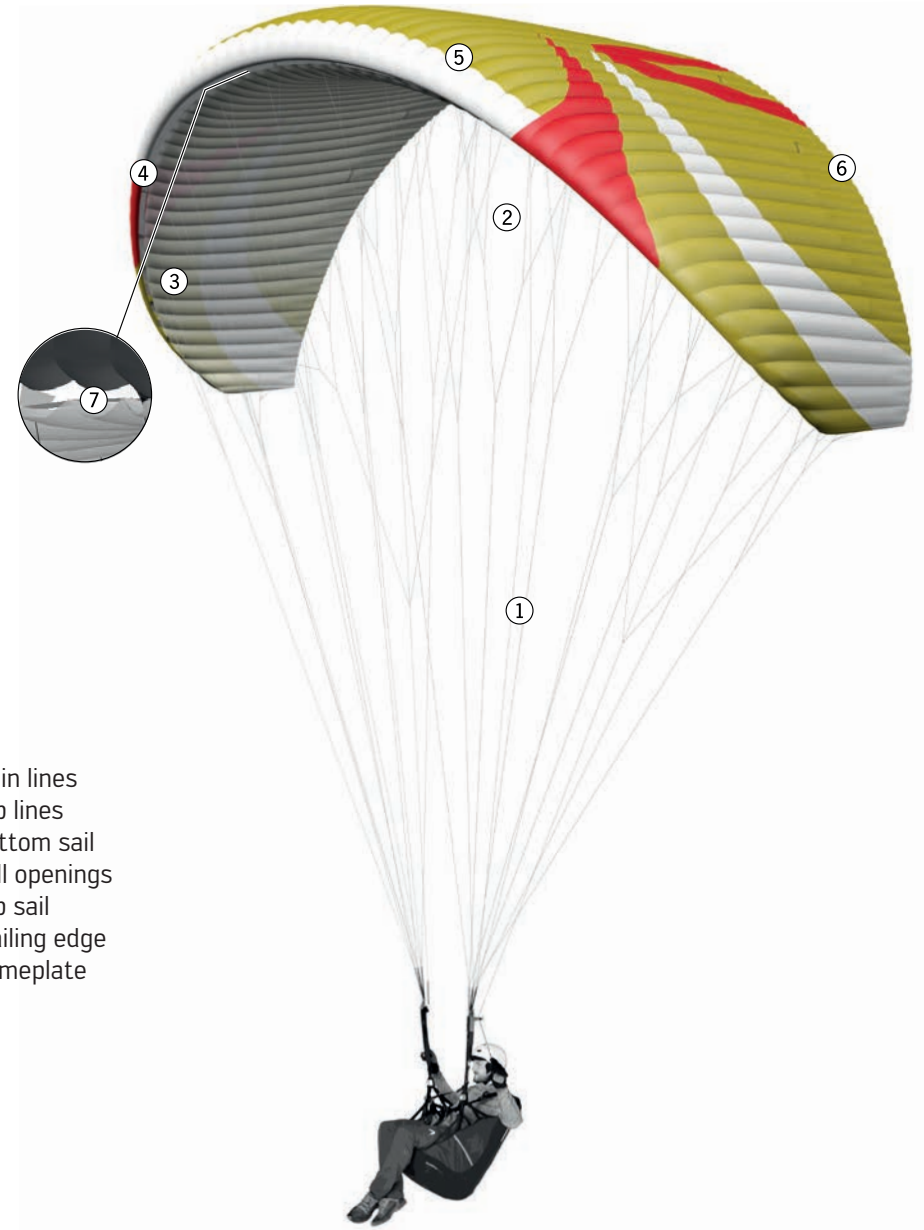
ARAK - XXS + XS



ARAK - S + M + L



OVERVIEW GLIDER



- 1 Main lines
- 2 Top lines
- 3 Bottom sail
- 4 Cell openings
- 5 Top sail
- 6 Trailing edge
- 7 Nameplate